

IN THE CLAIMS:

1. (currently amended) A WCDMA transceiver, comprising:
 - a transmit chain having a lookup table that provides coefficients to a digital predistorter based on power indicators; and
 - a predistorter training circuit, coupled to said transmit chain, that employs a receive chain of said WCDMA transceiver during a training mode to provide a digital compensation signal that is a function of an output of said transmit chain and employs both said power indicators and said digital compensation signal to cause said lookup table to provide alternative coefficients to said digital predistorter thereby to reduce distortion in said output.
2. (original) The transceiver as recited in Claim 1 wherein said transmit chain comprises:
 - an interpolator coupled to an output of said digital predistorter;
 - a digital to analog converter coupled to an output of said interpolator;
 - a low pass filter coupled to an output of said digital to analog converter;
 - a quadrature modulator coupled to an output of said low pass filter; and
 - an amplifier coupled to an output of said quadrature modulator.
3. (original) The transceiver as recited in Claim 1 wherein said receive chain comprises:
 - a quadrature De-modulator;
 - a low pass filter coupled to an output of said quadrature De-modulator; and
 - an analog to digital converter coupled to an output of said low pass filter.
4. (original) The transceiver as recited in Claim 1 wherein said predistorter training circuit comprises a coefficient update circuit to generate alternative power indicators for said

lookup table.

5. (original) The transceiver as recited in Claim 1 wherein said power indicators include both real and quadrature components.

6. (original) The transceiver as recited in Claim 1 wherein said predistorter training circuit operates only in a training mode.

7. (original) The transceiver as recited in Claim 1 wherein a root-raised cosine circuit provides said power indicator.

8. (currently amended) A method of reducing distortion in an output of a WCDMA transceiver, comprising:

employing a lookup table to provide coefficients to a digital predistorter of a transmit chain based on power indicators;

employing a receive chain of said WCDMA transceiver during a training mode to provide a digital compensation signal that is a function of an output of said transmit chain; and

employing both said power indicators and said digital compensation signal to cause said lookup table to provide alternative coefficients to said digital predistorter.

9. (original) The method as recited in Claim 8 wherein said transmit chain comprises:

an interpolator coupled to an output of said digital predistorter;

a digital to analog converter coupled to an output of said interpolator;

a low pass filter coupled to an output of said digital to analog converter;

a quadrature modulator coupled to an output of said low pass filter; and

an amplifier coupled to an output of said quadrature modulator.

10. (original) The method as recited in Claim 8 wherein said receive chain comprises:

a quadrature De-modulator;

a low pass filter coupled to an output of said quadrature De-modulator; and
an analog to digital converter coupled to an output of said low pass filter.

11. (original) The method as recited in Claim 8 wherein said employing both said power indicators and said digital compensation signal comprises generating alternative power indicators for said lookup table.

12. (original) The method as recited in Claim 8 wherein said power indicators include both real and quadrature components.

13. (original) The method as recited in Claim 8 wherein said employing both said power indicators and said digital compensation signal is carried out only in a training mode.

14. (original) The method as recited in Claim 8 wherein a root-raised cosine circuit provides said power indicator.

15. (currently amended) A WCDMA transceiver, comprising:

a transmit chain, including:

a digital predistorter,

a lookup table that provides coefficients to said digital predistorter based on power indicators,

an interpolator coupled to an output of said digital predistorter,

a digital to analog converter coupled to an output of said interpolator,

a low pass filter coupled to an output of said digital to analog converter,

a quadrature modulator coupled to an output of said low pass filter, and

an amplifier coupled to an output of said quadrature modulator;

a receive chain, including:

a quadrature De-modulator,

a low pass filter coupled to an output of said quadrature De-modulator,
an analog to digital converter coupled to an output of said low pass filter; and
a predistorter training circuit, coupled to said transmit chain, that employs said receive chain during a training mode to provide a digital compensation signal that is a function of an output of said transmit chain and employs both said power indicators and said digital compensation signal to cause said lookup table to provide alternative coefficients to said digital predistorter thereby to reduce distortion in said output.

16. (original) The transceiver as recited in Claim 15 wherein said predistorter training circuit comprises a coefficient update circuit to generate alternative power indicators for said lookup table.

17. (original) The transceiver as recited in Claim 15 wherein said power indicators include both real and quadrature components.

18. (original) The transceiver as recited in Claim 15 wherein said predistorter training circuit operates only in a training mode.

19. (original) The transceiver as recited in Claim 15 wherein a root-raised cosine circuit provides said power indicator.

20. (original) The transceiver as recited in Claim 15 wherein said transceiver is located within a cellular telephone.